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LIST OF REFERENCES CITED BY APPLICANT								
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U.S. PATENT DOCUMENTS								
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				<u> </u>			c Tunnel Junction and FET	
TH	AW	Roy SCHEUERLEIN, et al., "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in each Cell", 2000 IEEE International Solid-State Circuits Conference, Digest of Technical Papers, pgs. 128-129						
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	AZ		Additional References sheet(s) attack					
Examiner TU TU +10						Date Considered Sept 2004		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								